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JAN 26 2004
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ATTY. DOCKET NO.: 85170-4800
APPLICATION SERIAL NO.: 10/679,978

APPLICANT: Stephen Maxwell Parkes
FILING DATE: October 6, 2003
GROUP: 2819

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DC	AA	4,569,041	2/1986	Takeuchi et al.	370	60	
DC	AB	6,072,944	6/2000	Robinson	395	500.05	
	AC						
	AD						

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							YES	NO
DC	AE	EP 0 338 558	10/1989	Europe	—	—	X	
DC	AF	EP 0 867 820	1/1998	Europe	—	—	X	
DC	AG	EP 0 969 631	1/2000	Europe	—	—	X	
DC	AH	EP 1 096 736	5/2001	Europe	—	—	X	
DC	AI	WO 98/02822	1/1998	PCT	—	—	X	

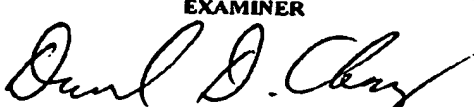
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

DC	AK	Parkes, "Space Wire: Links, Nodes, Routers and Networks" ECSS-E-50-12 Space Engineering pp. 1-101, 6/2000						
DC	AL	Swamy et al. "OO-VHDL, Extensions to VHDL" IEEE (1995) No month						
	AM	Guerrier et al. "A Generic Architecture for On-Chip Packet-Switched Interconnections"						
DC	AN	Benini et al. "Networks on Chips: A new SoC Paradigm" SOC Designs IEEE (2002) No month						
DC	AO	A. Tsutsui et al., "Special Purpose FPGA for High-speed Digital Telecommunication Systems", NTT Optical Network Systems Laboratories, pp 486-491, IEEE (1995) No month						
DC	AP	D'Angelo et al., "Modular Design of Communication Node Prototypes" pp. 170-175 IEEE (1997) No month						
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DC	AR	Vranesic "The FPGA Challenge", Dept. of Electrical and Computer Engineering, pp 121-126 IEEE (1998). No month						

EXAMINER
Paul D. Chang

DATE CONSIDERED
9/23/04

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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				85170-4800		10679978		
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				Stephen Maxwell Parkes				
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↑	AB	5,359,536	10/1994	Agrawal et al.	364	489		
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